PATENT 81788.0163

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Yoshiyuki Tanaka, et al.

Serial No: 09/130,818

Filed: August 7, 1998

METHOD FOR CONTROLLING NON-

VOLATILE SEMICONDUCTOR

MEMORY SYSTEM BY USING LOOK

UP TABLE

TRANSMITTAL OF FORMAL DRAWINGS

Attn: OFFICIAL DRAFTSPERSON

Mail Stop RCE

Commissioner for Patents

P.O. Box 1450

Alexandria , VA 22313-1450

Dear Sir:

Enclosed are the formal drawings (45 sheets - Figures 1-52) in the abovecaptioned case.

Respectfully submitted,

HOGAN & HARTSON L.L.P.

Date: March 3, 2004

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Art Unit:

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I hereby certify that this correspondence